

Notice of References Cited

Application/Control No.

09/661,912

Applicant(s)/Patent Under
Reexamination
AZIMI, SAEED

Examiner

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Art Unit

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tiberiu Chelcea and Steven Nowick, "A Low-Latency FIFO for Mixed-Clock Systems", 2000, IEEE, VLSI, 2000 Proceedings, IEEE Computer Society Workshop.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.